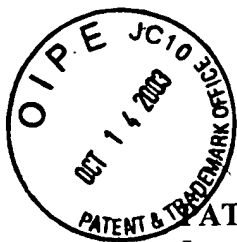


2633

#4  
S.I

10-21-03



IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Inventor(s): Charles Calvin Byers  
Daniel Raymond Hatcher

Case: 41-3

Serial No.: 09/932704

Filing Date: August 17, 2001

Examiner:

Group Art Unit: 2633

Title: Interconnecting Processing Units Of A Stored Program Controlled  
System Using Time Division Multiplexed Free Space Optics

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COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

SIR:

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR 1.97(b)(3)**

In accordance with 37 CFR 1.97(b)(3), the enclosed Information Disclosure Statement, with attached reference(s), is submitted for consideration in the above-identified application. This Information Disclosure Statement is made before the mailing date of a first office action on the merits.

Copies of the listed documents are enclosed.

**NO FEE IS REQUIRED**

In the event of any non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Lucent Technologies Deposit Account No. 12-2325** as required to correct the error.

Respectfully,

*Steven R. Santema*

Steven R. Santema, Attorney

Reg. No. 40156

630-979-7006

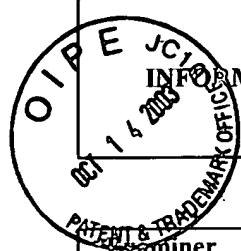
Date: October 9, 2003

Docket Administrator (Room 3J-219)

Lucent Technologies Inc.

101 Crawfords Corner Road

Holmdel, NJ 07733-3030



## INFORMATION DISCLOSURE STATEMENT

Case Name. C.C. Byers 41-3  
 Serial No. 09/932704  
 Applicant: C.C. Byers, et al.  
 Filing Date: August 17, 2001  
 Group: 2633

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## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA	5500523	March 19, 1996	Kenjiro Hamanaka	250	216	April 25, 1994
AB	5245680	September 14, 1993	Gerald F. Sauter	385	24	February 5, 1993
AC	6052498	April 18, 2000	Mario Paniccia	385	14	December 19, 1997
AD	5170269	December 8, 1992	Lin	359	9	May 31, 1991
AE	5204866	April 20, 1993	Block	372	27	February 5, 1992
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AG	5790291	August 4, 1998	Britz	359	159	December 7, 1995
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Document Number	Date	Country	Class	Subclass	Translation
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AK EP 0876019 A2	February 23, 1998	EPO	6	H04J	
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AN	Toshikazu, Sakano, et al; "Three-Dimensional Board-toBoard Free-Space Optical Interconnects and Their Application To The Prototype Multiprocessor System: Cosine-III" Applied Optics, Optical Society of America, Washington, D.C.; vol. 34, no. 11; April 10, 1995; pages 1815-1822, XP000497507; ISSN: 0003-6935
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EXAMINER

DATE CONSIDERED

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant